

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: John G. Heston
Serial No.: Unknown
Filing Date: October 28, 2003
Title: METHOD AND APPARATUS FOR
COMBINING MULTIPLE INTEGRATED
CIRCUITS


Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

INFORMATION DISCLOSURE STATEMENT

Pursuant to the provisions of 37 CFR §§ 1.56, 1.97 and 1.98, Applicant is enclosing two documents which may be material to examination of the present application. The enclosed documents are each listed on enclosed Form PTO-1449. Favorable consideration of the present application is respectfully requested.

Respectfully submitted,
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Attorneys for Applicant


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Date: October 28, 2003

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Enclosures: Form PTO-1449, and one copy
of each listed document

PTO-1449 Information Disclosure Citation In an Application	Application No. Unknown		Applicant(s) John G. Heston	
	Docket Number 004578.1377	Group Art Unit Unknown		Filing Date October 28, 2003

U.S. PATENT DOCUMENTS

		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	A						
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						
	J						
	K						
	L						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	M							
	N							
	O							
	P							

DOCUMENT (Including Author, Title, Source, Pertinent Pages, and Date)

Q	Xu, et al., "A 3-10-GHz GaN-Based Flip-Chip Integrated Broad-Band Power Amplifier", IEEE Transactions on Microwave Theory and Techniques, Vol. 48, No. 12, December 2000, pp. 2573-2578.
R	Debabrata Gupta, "A Novel Active Area Bumped Flip Chip Technology for Convergent Heat Transfer from Gallium Arsenide Power Devices", IEEE Transactions on Components, Packaging, and Manufacturing Technology, Part A, Vol. 18, No. 1, March 1995, pp.82-86.
S	

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

U.S. PATENT AND TRADEMARK OFFICE